

## DUAL P-CHANNEL MATCHED MOSFET PAIR

#### **GENERAL DESCRIPTION**

The ALD1102 is a monolithic dual P-channel matched transistor pair intended for a broad range of analog applications. These enhancement-mode transistors are manufactured with Advanced Linear Devices' enhanced ACMOS silicon gate CMOS process.

The ALD1102 offers high input impedance and negative current temperature coefficient. The transistor pair is matched for minimum offset voltage and differential thermal response, and it is designed for switching and amplifying applications in +2V to +12V systems where low input bias current, low input capacitance and fast switching speed are desired. Since these are MOSFET devices, they feature very large (almost infinite) current gain in a low frequency, or near DC operating environment. When used with an ALD1101, a dual CMOS analog switch can be constructed. In addition, the ALD1102 is intended as a building block for differential amplifier input stages, transmission gates, and multiplexer applications.

The ALD1102 is suitable for use in precision applications which require very high current gain, beta, such as current mirrors and current sources. The high input impedance and the high DC current gain of the Field Effect Transistors result in extremely low current loss through the control gate. The DC current gain is limited by the gate input leakage current, which is specified at 50pA at room temperature. For example, DC beta of the device at a drain current of 5mA at  $25^{\circ}$ C is = 5mA/50pA = 100,000,000.

#### FEATURES

- Low threshold voltage of 0.7V
- Low input capacitance
- Low Vos grades -- 2mV, 5mV, 10mV
- High input impedance -- 10<sup>12</sup>Ω typical
- · Low input and output leakage currents
- Negative current (IDS) temperature coefficient
- Enhancement-mode (normally off)
- DC current gain 10<sup>9</sup>

#### **ORDERING INFORMATION**

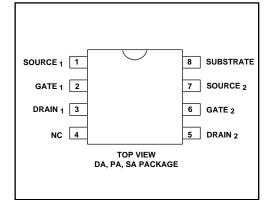
Operating Temperature Range*									
-55°C to +125°C	0°C to +70°C	0°C to +70°C							
8-Pin CERDIP Package	8-Pin Plastic Dip Package	8-Pin SOIC Package							
ALD1102 DA	ALD1102A PA ALD1102B PA ALD1102 PA	ALD1102 SA							

\* Contact factory for industrial temperature range

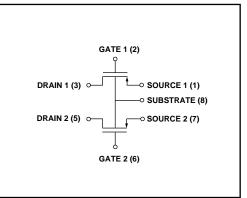
#### APPLICATIONS

- Precision current mirrors
- Precision current sources
- Analog switches
- Choppers
- Differential amplifier input stage
- Voltage comparator
- Data converters
- · Sample and Hold
- · Analog inverter

#### **PIN CONFIGURATION**



#### **BLOCK DIAGRAM**



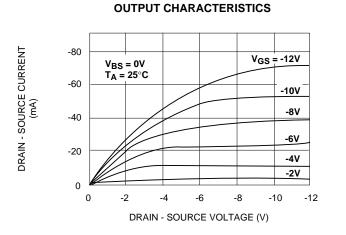
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### ABSOLUTE MAXIMUM RATINGS

Drain-source voltage, V <sub>DS</sub>	
Power dissipation	500 mW
Operating temperature range PA, SA package	0°C to +70°C
DA package	55°C to +125°C
Storage temperature range	-65°C to +150°C
Lead temperature, 10 seconds	+260°C

# OPERATING ELECTRICAL CHARACTERISTICS $T_A = 25^{\circ}C$ unless otherwise specified

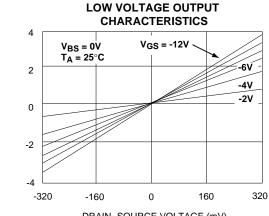
Parameter S		1102A				1102B		1102			Test	
	Symbol	Min	Тур	Max	Min	Тур	Max	Min	Тур	Max	Unit	Conditions
Gate Threshold Voltage	V <sub>T</sub>	-0.4	-0.7	-1.2	-0.4	-0.7	-1.2	-0.4	-0.7	-1.2	V	$I_{DS}$ = -10µA $V_{GS}$ = $V_{DS}$
Offset Voltage V <sub>GS1</sub> - V <sub>GS2</sub>	V <sub>OS</sub>			2			5			10	mV	$I_{DS}$ = -100 $\mu$ A $V_{GS}$ = $V_{DS}$
Gate Threshold Temperature Drift	TC <sub>VT</sub>		-1.3			-1.3			-1.3		mV/°C	
On Drain Current	I <sub>DS (ON)</sub>	-8	-16		-8	-16		-8	-16		mA	$V_{GS} = V_{DS} = -5V$
Transconductance	G <sub>fs</sub>	2	4		2	4		2	4		mmho	$V_{DS} = -5V I_{DS} = -10mA$
Mismatch	$\Delta G_{\text{fs}}$		0.5			0.5			0.5		%	
Output Conductance	G <sub>OS</sub>		500			500			500		μmho	V <sub>DS</sub> = -5V I <sub>DS</sub> = -10mA
Drain Source ON Resistance	R <sub>DS(ON)</sub>		180	270		180	270		180	270	Ω	$V_{DS} = -0.1V V_{GS} = -5V$
Drain Source ON Resistance Mismatch	$\Delta R_{DS(ON)}$		0.5			0.5			0.5		%	V <sub>DS</sub> = -0.1V V <sub>GS</sub> = -5V
Drain Source Breakdown Voltage	BV <sub>DSS</sub>	-12			-12			-12			v	I <sub>DS</sub> = -10μΑ V <sub>GS</sub> =0V
Off Drain Current	I <sub>DS(OFF)</sub>		0.1	4 4		0.1	4 4		0.1	4 4	nA μA	$V_{DS} = -12V V_{GS} = 0V$ $T_A = 125^{\circ}C$
Gate Leakage Current	I <sub>GSS</sub>		1	50 10		1	50 10		1	50 10	pA nA	V <sub>DS</sub> =0V V <sub>GS</sub> =-12V T <sub>A</sub> = 125°C
Input Capacitance	C <sub>ISS</sub>		6	10		6	10		6	10	pF	



## **TYPICAL PERFORMANCE CHARACTERISITCS**

DRAIN-SOURCE CURRENT (mA)

DRAIN-SOURCE CURRENT

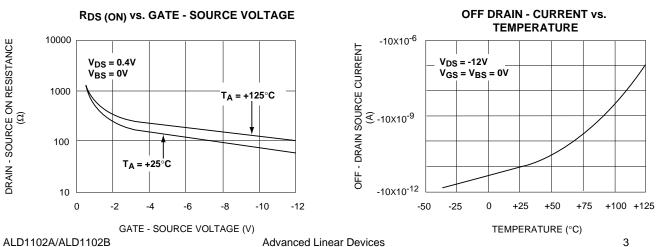


DRAIN -SOURCE VOLTAGE (mV)

TRANSFER CHARACTERISTIC

FORWARD TRANSCONDUCTANCE vs. DRAIN - SOURCE VOLTAGE 10000 FORWARD TRANSCONDUCTANCE I<sub>DS</sub> = -5mA  $V_{BS} = 0V$ f = 1KHz 5000 2000 (oquud) 1000 T<sub>A</sub> = +125°C +25°C TΑ 500 I<sub>DS</sub> = -1mA 200 100 0 -2 -4 -6 -8 -10 -12 DRAIN - SOURCE VOLTAGE (V)

WITH SUBSTRATE BIAS -20  $V_{BS} = 0V$ 4V 2V -15 6V 8V 100 ·12V (HA) -10 -5  $V_{GS} = V_{DS}$ T<sub>A</sub> = 25°C 0 0 -0.8 -1.6 -2.4 -3.2 -4.0 GATE - SOURCE VOLTAGE (V)



ALD1102